![Valon Technology logo]

Quick Start Guide – 3010a Programmable Frequency Divider and Clock Source

Your 3010a Programmable Frequency Divider module is ready to go. Apply 5~6Vdc with the dc cable provided and the input signal you want to divide. The jumpers have been preset to provide **divide-by-12 on all outputs**. You can change your divider settings by moving the 2mm jumpers provided. It is not necessary to power off the divider module when changing jumper settings.

Divider **programming table**:

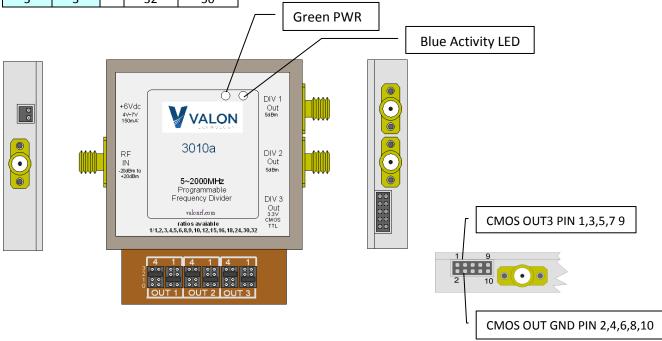| MSB (4)	LSB (1)		divide by	duty cycle
0	0		1	direct
0	1		2	50
0	2		3	33
0	3		4	50
1	0		5	50
1	1		6	50
1	2		8	50
1	3		9	44
2	0		10	50
2	1		12	50
2	2		15	47
2	3		16	50
3	0		18	50
3	1		24	50
3	2		30	50
3	3		32	50

Note that this table applies to all three dividers. The divide-by -1 function allows the divider to act as a **buffer amplifier**. The input signal level can be from typically less than -30dBm to a maximum of +13dBm.

When power is applied, the green PWR LED will be illuminated (above the O in Valon). If a valid input signal is present, the blue Activity LED will be visible in the location shown.

Divider 3 output is available on the 10-pin 2mm header. Pin 9 is Divider 3 OUT. Pin 1,3,5,and 7 are the Divider 3 output with an additional divide by 16,8,4, and 2.

More information is at:
https://www.valonrf.com/uploads/1/1/7/3/117370920/valon_3010a_specification_20190208.pdf

Green PWR

Blue Activity LED

CMOS OUT3 PIN 1,3,5,7 9

CMOS OUT GND PIN 2,4,6,8,10